

IN THE U.S. PATENT AND TRADEMARK OFFICE May 2, 1995

Applicant(s): Yasumitsu TSUTSUI, et al

Title : ADDRESS PATTERN GENERATOR

Serial No. : 08/026 999 Group : 2313

Filed: March 5, 1993 Examiner: Snyder

The Commissioner of Patents and Trademarks Washington, D. C. 20231

Sir:

Herewith is an amendment in the above-identified application.

- [] Verified Statement(s) re small entity status submitted previously.
- [] ____ Verified Statement(s) re small entity status enclosed.
- [X] No additional filing fee is required.
- The additional filing fee has been calculated as shown below:

	After	•	No. I	aid	No.				re ()	Add.
For	Amendme	ent F	revio	usly	Extra		LG	Entity	SM Entity	<u>Fee</u>
Total	(8	-	20	=	0)	-	x	\$ 22.00	x \$ 11.00 ¦	\$0.00
Indep.	. (1	-	3	=	0)	!	x	\$ 76.00	x \$ 38.00 ¦	0.00
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- [X] A check for \$_870.00 is enclosed to cover fees.
- [X] Please credit any overpayment, or charge any additional filing fee or application processing fee required under 37 CFR 1.16 or 1.17 by this communication, to Deposit Account No. 06-1382. A duplicate copy of this sheet is enclosed.

IN DUPLICATE

Respectfully submitted,

DSG/gc

Encl: Listed Above

David S. Goldenberg

Reg. No. 31 257

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on May 2, 1995.

Respectfully submitted,

David S. Goldenberg

130.9210

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Atty. Docket

No.: OPS Case 259

The Commissioner of Patents and Trademarks Washington, D. C. 20231

LETTER TO THE OFFICIAL DRAFTSPERSON

Sir:

Proposed amendments are illustrated in red on the accompanying informal drawing sheets for this application. Should this application be deemed in condition for allowance, the Applicants will submit formal drawings with these amendments entered thereon.

Figure 1

For element 3, replace "MUXIMUM" with ---MAXIMUM--- to correct a spelling error identified by the Examiner.

Figure 2

This figure has been amended to illustrate the row address generator and its interconnection to the column address The structure of the row address generator and its relationship to the column address generator are described in the specification and illustrated elsewhere in the drawings. order to make the amendments to Figure 2, the Applicants have transferred Figure 2 to a separate drawing sheet.

Respectfully submitted,

DSG/gc

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Figures 1 and 2 with changes indicated in red Encl: